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(d) Figure 1.4 shows a circuit with two D-flipflops FF1 and FF2 with setup and hold times of 3 ns and 2 ns respectively, and a clock-to-Q output delay of 1 ns. The clock signal CLK has a 1:1 mark-space ratio. The D input of FF2 is driven by logic\_A, which has a propagation delay between 1 ns and 4 ns. The clock input to FF2 is

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1. (a) Figure 1.1 shows a circuit with two D flip-flops FF1 and FF2 with setup and hold times of 2 ns and 1 ns respectively, and a clock-to-Q output delay of 2 ns. The clock signal CLK has a 1:1 mark-space ratio. The signal path has two combinational circuits, LUT\_1 and LUT\_2, each having a propagation delay between 1 ns and 5 ns.

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## Nyquist–Shannon sampling theorem - Wikipedia

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## Brain-computer interface - Wikipedia

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### Lempel–Ziv–Welch - Wikipedia

1958, H. A. Simon and Allen Newell: "within ten years a digital computer will be the world's chess champion" and "within ten years a digital computer will discover and prove an important new mathematical theorem." 1965, H. A. Simon: "machines will be capable, within twenty years, of doing any work a man can do."

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