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Product Version 5.0 July 2002 2000

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January 2004 3 Product Version 5.0  
Preface ...

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Cadence Circuit Simulator Device Model  
Equations manual. The Spectre circuit  
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under the Cadence® design framework II.  
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Spectre Circuit Simulator Reference

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CMOS circuits. This manual will walk you through all the necessary steps for designing and testing an inverter. First, we are going to create a schematic for the inverter. We, then, create a symbol for the inverter

Cadence Manual - Penn Engineering

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Spectre Circuit Simulator Device Model Equations manual. The Spectre circuit simulator is often run within the Cadence® analog circuit design environment, under the Cadence® design framework II. To see how the Spectre circuit simulator is run under the analog circuit design environment, read the Cadence Analog

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Cadence User Guide - modularscale.com  
The Cadence main window (Common

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