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~~Computer Logic Design M Morris Mano Part 2 Q. 1.33: The state of a 12-bit register is 100010010111. What is its content if it represents~~
Digital Design: Q. 1.13: Do the following conversion problems: (a) Convert decimal 27.315 to binary Solutions Manual Digital Design
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Q. 4.13: The adder-subtractor circuit of Fig. 4.20(b) has the following values for mode input M and *Standard Books for ISRO, IES & GATE Preparation for ECE Digital Design 5th Edition Mano*

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Errata: Suspected Typos in M. Morris Mano & Michael D. Ciletti's Digital Design: with an Introduction to the Verilog HDL (5th edition, 1st printing) Most of the typos listed here have been fixed in the 6th edition.

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