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He has published over 650 papers, is inventor or coinventor of 31 issued US Patents, has authored the text book VLSI Digital Signal Processing Systems: Design and Implementation (Wiley, 1999), and is the co-editor (with Takao

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Nishitani) of the  
reference book Digital  
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18.3 Important Features

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processors are designed  
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Verilog Hardware  
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Digital Signal Processing are also revealed. With a comprehensive coverage of topics the book provides an appropriate mix of examples to illustrate the design methodology. Key Features: A practical guide to designing efficient digital systems, covering the complete spectrum of digital design from a digital

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streaming applications,  
giving examples of  
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Alliance) awards in  
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## Designing VLSI

systems represents a  
challenging task. It is a  
transformation among  
different specifications  
corresponding to  
different levels of  
design: abstraction,  
behavioral, structural  
and physical. The

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behavioral level describes the functionality of the design. It consists of two components; static and dynamic. The static component describes operations, whereas the dynamic component describes sequencing and timing. The structural level contains information about components, control and

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connectivity. The physical level describes the constraints that should be imposed on the floor plan, the placement of components, and the geometry of the design. Constraints of area, speed and power are also applied at this level. To implement such multilevel transfonnation, a design

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methodology should be devised, taking into consideration the constraints, limitations and properties of each level. The mapping process between any of these domains is non-isomorphic. A single behavioral component may be transformed into more than one structural component. Design methodologies are the

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most recent evolution in the design automation era, which started off with the introduction and subsequent usage of module generation especially for regular structures such as PLA's and memories. A design methodology should offer an integrated design system rather than a set of separate unrelated routines and

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tools. A general outline of a desired integrated design system is as follows: \* Decide on a certain unified framework for all design levels. \* Derive a design method based on this framework. \* Create a design environment to implement this design method.

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selection of multimedia applications, programmable and custom architectures for the implementations of multimedia systems, and arithmetic architectures and design methodologies. The book covers recent applications of digital signal processing algorithms in multimedia, presents

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high-speed and low-  
priority binary and finite  
field arithmetic  
architectures, details  
VHDL-based  
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approaches, and more.

DSP Integrated Circuits  
establishes the essential  
interface between theory  
of digital signal

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and their  
Processing  
implementation in full-  
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technology. With an  
emphasis on techniques  
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algorithms and  
hardware in order to  
achieve high  
performance in terms of  
throughput, low power  
consumption, and  
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provides the professional engineer, researcher, and student with a firm foundation in the theoretical as well as the practical aspects of designing high performance DSP integrated circuits.

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processor, a 2-D  
Discrete Cosine  
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and a wave digital filter  
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The case studies cover  
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down manner, from  
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algorithm design and  
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of optimal architectures, realization of processing elements, to the floor-planning of the integrated circuit.

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distributed arithmetic  
Presents the design  
process in a top-down  
manner and incorporates  
numerous problems and  
solutions

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a set of forthcoming  
books focussed on state-  
of-the-art development  
in the VLSI Signal  
Processing area. It is a  
response to the

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tremendous research activities taking place in that field. These activities have been driven by two factors: the dramatic increase in demand for high speed signal processing, especially in consumer electronics, and the evolving microelectronic technologies. The available technology has

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always been one of the main factors in determining algorithms, architectures, and design strategies to be followed. With every new technology, signal processing systems go through many changes in concepts, design methods, and implementation. The goal of this book is to introduce the reader to

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the main features of  
VLSI Signal Processing  
and the ongoing  
developments in this  
area. The focus of this  
book is on: • Current  
developments in Digital  
Signal Processing (DSP)  
processors and  
architectures - several  
examples and case  
studies of existing DSP  
chips are discussed in  
Chapter 1. • Features

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and requirements of image and video signal processing architectures - both applications specific integrated circuits (ASICs) and programmable image processors are studied in Chapter 2. • New market areas for signal processing - especially in consumer electronics such as multimedia, teleconferencing, and

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Impact of arithmetic circuitry on the performance of DSP processors - several topics are discussed in Chapter 3 such as: number representation, arithmetic algorithms and circuits, and implementation.

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